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Title: A Method And System For Logic

Equivalence Checking Docket No.: 19329-719

Sheet 1 of 5

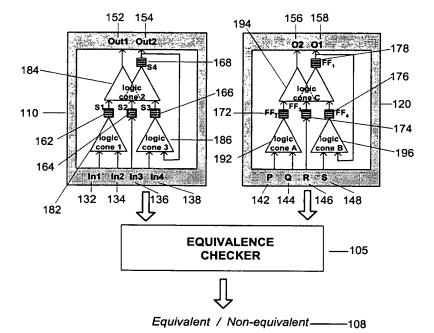


Figure 1: Equivalence checking of two designs with identical state encoding

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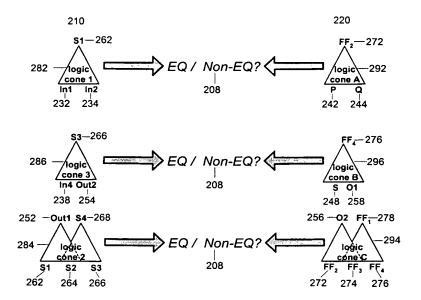


Figure 2: Comparison of logic cones of Design 1 and Design 2.

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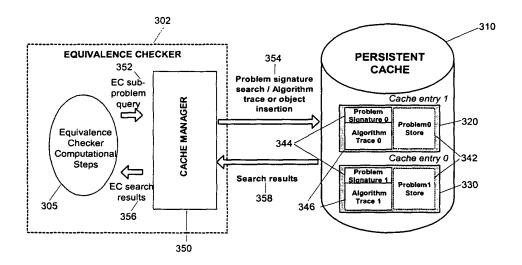


Figure 3: Persistent caches for adaptive learning

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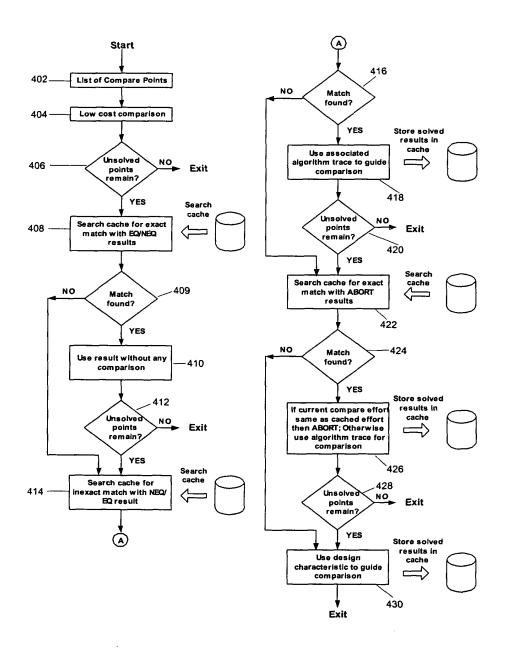


Figure 4: Compare flow with a persistent cache

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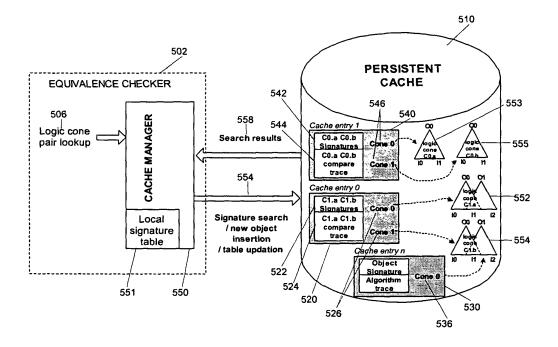


Figure 5: Logic cone pair caching in the persistent cache